NATIONAL INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

LIST OF ELIGIBLE APPLICANTS AND INTERVIEW NOTICE

PROJECT: JKSTIC "ARCHITECTURAL & TECHNOLOGY-DEPENDENT OPTIMIZATION OF DSP FILTERS FOR FPGA BASED IMAGE PROCESSING APPLICATIONS"

THE FOLLOWING CANDIDATES HAVE BEEN FOUND ELIGIBLE FOR APPEARING IN THE INTERVIEW FOR THE ABOVE MENTIONED PROJECT.

S. No	Name
1	Ubair Ali
2	Zainubia
3	Gazala Gul
4	Kamran Iqbal Naqash
5	Bashrat Ali
6	Shaika Hassan
7	Sheena Tahir
8	Mubashir Yaseen
9	Umaira Khanam
10	Naira Nafees

DATE OF INTERVIEW: MARCH 12, 2024, TUESDAY

TIME: 09 30 AM

VENUE: COMMITTEE ROOM

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

ALL THE CANDIDATES ARE REQUESTED TO BRING ORIGINAL CERTIFICATES AND ANY OTHER DOCUMENTS WITH THEM.

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(Dr G R Begh)
Principal Investigator

Dated: March 06, 2024