



DEPARTMENT OF ELECTRICAL ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR

No: NIT/Elect/19/224

Dt: 02/05/2019

Shortlisted candidates for the engagement as project assistant in the CPRI sponsored project
“Model Order Reduction for Simulation Acceleration in Power Electronics”

S. No	Name	Eligibility Status	Shortlisted for Interview (Yes/No)
1	Aadil Nabi Lolpuri	Not eligible; B. Tech % < 80%	No
2	B V S S Sai Gopal	Eligible	Yes
3	Sushant Verma	Not eligible; M. Tech % < 80%	No
4	Abhay Kumar Singh	Eligible	Yes
5	Araib Irshad	Not eligible; B. Tech CGPA < 8	No
6	Naira Jeelani	Eligible	Yes
7	Irfan Ahmad Gandroo	Not eligible; B. Tech % < 80%	No
8	Khalid bin Bashir	Not eligible; Degree not relevant	No
9	Syed Sajid Rizvi	Eligible	Yes
10	Asif Altaf Shah	Not eligible; Degree not relevant	No
11	Adil Mohiuddin	Eligible	Yes
12	Bazila Mushtaq	Eligible	Yes
13	Ifrah Parvez Bhat	Eligible	Yes
14	Arif Nabi	Not eligible; GATE not qualified	No
15	Saima Ashraf	Eligible	Yes
16	Naheed Mobin	Eligible	Yes

Interview of the shortlisted candidates will be held on **13th May 2019 from 10:00 AM** onwards in the Department of Electrical Engineering, NIT Srinagar.

Note:

1. All shortlisted candidates should bring all relevant certificates **in original** at the time of interview.
2. No TA/DA shall be paid for attending the interview.

Dr. Mohammad Abid Bazaz
Principal Investigator
Associate Professor, EED