

NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

HAZRATBAL SRINAGAR 190 006 (J&K)

Advertisement No: ECE /NIT/147C DT: February 15, 2024 ADVERTISEMENT FOR JRA RECRUITMENT UNDER JKSTIC PROJECT

Project title	Architectural & Technology-Dependent optimization of DSP Filters for FPGA based Image Processing applications
Sponsoring agency	J&K Science Technology and Innovation Council (JKSTIC) Department of Science and Technology, Government of J&K
Position title	Junior Research Assistant
Duration	Initially for a period of 1year but likely to extend for 24 months (2 years)
Number of position(s)	1 (One)
Essential Qualification	 B.E. /B. Tech. in ECE or relevant degree; with a minimum CGPA of 6.5 or not less than 60%. M. E. / M. Tech in related field and who have done Major project related to Signal Processing / DSP Architecture; with a minimum CGPA of 6.5 or not less than 60%. OR BE/B. Tech (4-year) with valid GATE score above the prescribed cut off level / NET Qualification. The candidates shall have a minimum CGPA of 8.0 or not less than 75%.
Desirable	Prior knowledge of Hardware Description Language.
Emoluments	Rs. 20,000/- Per month
Registration for PhD	The selected candidate can register for PhD programme if eligible, as per institute rules and statutes
How to apply?	The format of application is attached with this advertisement. Complete application form, brief CV (maximum 3 pages) and scanned copies of certificates (All certificates should be a single file in pdf format) be send via email to: grbegh@nitsri.ac.in and burhan@nitsri.ac.in Incomplete Application forms will be rejected without any information
Submission mode	Soft copy
Last date to apply	25/02//2024
Contact	1. Gh. Rasool Begh Associate Professor, ECE Department National Institute of Technology Srinagar - 190006 Jammu and Kashmir grbegh@nitsri.ac.in 9906577743 2. Burhan Khurshid Assistant Professor, ECE Department National Institute of Technology Srinagar - 190006 Jammu and Kashmir burhan@nitsri.ac.in 9797875163

Interested and eligible candidates may send their application (scanned copy in pdf Format) as per the format given below along with the CV, and copies of certificates to grbegh@nitsri.ac.in and burhan@nitsri.ac.in by 25-02-2024.

Please mention the subject line of your email as

Application for the JKSTIC: JRA position

The 3 files should be specifically in pdf format and be named as:

- 1. Your Name_application.pdf
- 2. Your Name_CV.pdf
- **3.** Your Name_Certificates.pdf
 - a. Scanned copies of B Tech and M Tech certificates
 - b. Scanned copy of Date of Birth
 - **c.** Scanned copies of Proof for research experience, special achievements and publications, if any.

No other format will be accepted, and applications will be rejected

Shortlisted / screened candidates will have to attend the interview at NIT Srinagar.

The interview date will be communicated to the screened candidates through e-mail or phone or Institute website.

No TA/DA will be paid for attending the interview

For any clarifications, candidates can contact the Principle Investigator.

Gh Rasool Begh

Principal Investigator (project)

Associate Professor, Electronics & Communication Engineering Department

National Institute of Technology Srinagar

Jammu and Kashmir-190006, India

EMAIL: GRBEGH@NITSRI.AC.IN

MOBILE: 9906577743

Format of Application Form



NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR 190006 Application Form

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