



**NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR**  
HAZRATBAL KASHMIR -190006 (J & K)

**Advertisement Notice No.01 of 2020**  
**Dated:20.01.2020**

Applications are invited for the following position in the project "Development and Augmentation of the Industry Standard ASM-GaN-HEMT Model for State-of-the-art Integrated Circuit Design" under the supervision of Dr. Sheikh Aamir Ahsan, Department of Electronics & Communication Engineering NIT Srinagar; sponsored by Science & Engineering Research Board (SERB) Department of Science & Technology Govt. of India, under the scheme Startup Research Grant. The post is on temporary basis for a period of 1 year, but is likely to continue for the duration of the project (2 years).

Post	Job Requirement	Qualifications	
		Essential	Desirable
Junior Research Fellow – JRF (1 post)	Execution of the Project "Development and Augmentation of the Industry Standard ASM-GaN-HEMT Model for State-of-the-art Integrated Circuit Design"	1. First class B.E. /B. Tech or 6.5 CGPA in Electronics & Communication Engineering/ Electrical Engineering with GATE qualified; 2. First class or 6.5 CGPA in M.E. / M. Tech in Electronics & Communication or in relevant branch of Electronics & Communication Engineering; and 3. The applicant should have studied Semiconductor Device (Modeling / Simulation / Characterization) related subjects at B. Tech and /or M. Tech level.	1. Specialization in Semiconductor Device Physics, Modeling & Simulation. 2. Working Knowledge of TCAD / Modeling tools such as Silvaco / Sentaurus / IC-CAP / ADS. 3. Working knowledge of SPICE circuit simulations.

Interested candidates may apply with a hard copy/email (sheikh@nitsri.ac.in) of resume/CV mentioning educational qualifications, professional experience (if any), contact details including email and phone numbers, and Certificates, etc. to the Principal Investigator, "Development and Augmentation of the Industry Standard ASM-GaN-HEMT Model for State-of-the-art Integrated Circuit Design" Electronics & Communication Engineering Department at the address given below latest by 29<sup>th</sup>, Jan 2020. The list of screened, short listed applicants will be available on the Institute website/local dailies. The Interview shall be held at NIT Srinagar on 4<sup>th</sup>, Feb 2020. The emoluments will be as per the MHRD / SERB rules (without HRA, as approved by the SERB budget approval committee). The tenure of the post will be coterminous with the duration of the project.

  
20/1/20  
Registrar  
20-01-2020